



Low Power VLSI Design: Fundamentals

By Angsuman Sarkar

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This book teaches basic and advanced concepts, new methodologies and recent developments in VLSI technology with a focus on low power design. It provides insight on how to use Tanner Spice, Cadence tools, Xilinx tools, VHDL programming and Synopsis to design simple and complex circuits using latest state-of-the art technologies. Emphasis is placed on fundamental transistor circuit-level design concepts.

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Low Power VLSI Design: Fundamentals By Angsuman Sarkar Bibliography

- Rank: #9402115 in Books
- Published on: 2016-07-31
- Original language: English
- Number of items: 1
- Dimensions: 9.60" h x .90" w x 6.90" l, .0 pounds
- Binding: Hardcover
- 466 pages

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Editorial Review

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